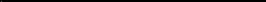


U.S. UTILITY Patent Application

**PATENT NUMBER and
ISSUE DATE**

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10074732	02/13/2002	257	250	2015 2011	 Eddie Lee

****APPLICANTS:** Mathew Leo; Nguyen Bich-Yen; Sadd Michael; White Bruce;

Brück

****CONTINUING DATA VERIFIED:**

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH	<input type="checkbox"/>	RESCIND	<input type="checkbox"/>
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	SC11805TP	
Verified and Acknowledged Examiners's initials				
TITLE : Method of forming a vertical double gate semiconductor device and structure thereof				
U.S. DEPT. OF COMM./PAT & TM-PTO-438L (Rev. 12-94)				

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NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE				DRAWING
Amount Due	Date Paid	Sheets Draw.	Fig.s Drawn.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED FOR ISSUE		
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				
Application Examiner				

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